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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JUL 11 2006

Appl. No. : 10/829,380 Confirmation No. 7082

Applicant : A. HAYASHI et al

Filed : April 22, 2004

Title : A SEMICONDUCTOR INTEGRATED CIRCUIT
CONTROLLING OUTPUT IMPEDANCE AND SLEW RATE

TC/AU : 2816

Examiner : T.T. Lam

Docket No. : H-1139

Customer No.: 24956

Commissioner for Patents
Mail Stop AF
P.O. Box 1450
Alexandria, VA 22313-1450AMENDMENT

Sir:

In response to the Office Action (Final Rejection) of April 11, 2006, please
amend the above-identified application as follows.

Amendments to the Claims are reflected in the listing of claims which begins on
page 2 of this paper.

Remarks / Arguments begin on page 8 of this paper.

Appendix including PTO-1449 Form filed 4/22/04 is attached following page 11 of
this paper.

